



Class	327
Subclass	200
Issue Classification	



PATENT NUMBER
6377096
6377096

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>AC</i> <i>TH</i>	APR 23 2002

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/696104		327	200	2816	<i>Long Nguyen</i>

APPLICANT'S NAME: Samuel T. Naffziger

TITLE: *System for dynamic logic interface circuit*

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS-REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
327	200	327	211	212	
INTERNATIONAL CLASSIFICATION					
H 03 K	3/12				
H 03 K	3/037				
H 03 K	3/286				
H 03 K	3/356				

☐ Continued on Issue Slip Inside File Jacket

3-25-02 *Formal Drawings (1 sheet) set* *12-4-00*

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drawg.	Figs. Drawg.	Total Claims	Print Claim for O.G.
			3	1
	NOTICE OF ALLOWANCE MAILED			
<input type="checkbox"/> The term of this patent subsequent to <i>10/25/01</i> (date) has been disclaimed.	<i>Long Nguyen</i> (Assistant Examiner)		<i>10-26-01</i>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. <i>6,000,000</i>	<i>Toan Tran</i> (Primary Examiner)		ISSUE FEE	
<input type="checkbox"/> The term of this patent have been disclaimed.	<i>10/25/01</i> (Date)		Amount Due <i>12,800.00</i>	
	<i>10/25/01</i> (Date)		Date Paid <i>9/13/02</i>	
	<i>10/25/01</i> (Date)		ISSUE BATCH NUMBER	
	<i>10/25/01</i> (Date)		<i>005</i>	

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